These boards are used in numerous applications ranging from general purpose processing, signal analysis, signal generation, software radio, radar, navigation, communication, medical electronics, sonar, audio systems, and so-on.

**Schematics**

The PCI-DSP-FPGA series of boards have 3 Xilinx Virtex-4 FGPAs in addition to a DSP Processor (ADSP-TS201). It can function as a stand-alone processing board or can be plugged in and controlled via PCI interface. The FPGAs can be loaded through the JTAG port, PCI interface, or the configuration can be stored in the EPROM for loading on power-up. Similarly, the DSP software can also be loaded through the PCI interface or via an EPROM on power-up.

The communication FPGA on the board handles the PCI interface. The 2 processing FPGAs are high-performance FPGAs that can be used for Digital Logic/DSP applications. The FPGAs communicate with the DSP using high-speed Link Ports. Communication can be interrupt based or polling based depending upon user requirements. One of the FPGAs is also interfaced with 2 16-bit ADCs and DACs. Also, high speed Digital IOs are provided for all FPGAs.

The board is capable of independent operation through an USB interface implemented in DSP without involving a PCI bus. The FPGAs and DSPs can be loaded through EPROM or through a PCI interface. Special efforts have been made to ensure high-speed communication within and outside the board. The FPGAs are connected directly to a pair of high speed serializers enabling data transfer at the rate of 6.1 Gb/s.

The board is rich with debug features for application development. All the FPGAs are connected to debug ports in order to monitor FPGA activity. Also, a specialized connector for connecting the ADSP-ICE (In-Circuit-Emulator) is provided, which can be used to debug the DSP code.
**DSP/FPGA Processing Boards**

**Features**
- Stand-alone and pluggable FPGA board with PCI Interface / USB Interface
- On-board clock with External Sync
- 3 Xilinx Virtex-4 FPGAs
  - General Purpose Communication FPGA for PCI Interface
  - 2 High Performance FPGA for Logic/DSP Applications (approx. M gates)
- 1 ADSP-TigerSharc201 Processor
  - Operates at 600 MHz
  - 3 MB internal and 128 MB external memory
  - 4 High Speed Link Ports for communication
- 2 x 16-bit ADCs—AD 9446*, 100 MSPS sampling rate
- 2 x 16-bit DACs—AD9777* 160 MSPS/400MSPS input/output data rate. Interpolating filter
- High Speed Serial Digital Output (6.1 Gb/s)
- Debug Features Include:
  - Debug Ports for all FPGAs
  - Debug Port for connecting ADSP In-Circuit Emulator for DSP
- Board Dimension: 8” x 11”
- Power Requirements:
- PCI Driver provided
- Several signal processing and signal generation cores available

**Applications**
- General Purpose Processing
- Signal Analysis
- Signal Generation
- Software Radio
- EW application
- Radar
- Navigation
- Communication
- Medical Electronics
- Sonar
- Audio Systems

**PCI-DSP-FPGA-1DSP2FPGA-500CLOCK-06**